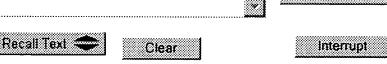
#### Search Results -

Terms	Documents
(("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory)	28

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

Database:



## Search History

DATE: Thursday, December 29, 2005 Printable Copy Create Case

Set Name Query

side by side

DB=PGPB, USPT, USOC; PLUR=YES; OP=OR

Hit Count Set Name result set

Refine Search

L1 (("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory) 28 L1

#### Search Results -

Terms	Documents			
L1 and L3	15			

US Patents Full-Text Database
US OCR Full-Text Database

EPO Abstracts Database

JPO Abstracts Database
Derwent World Patents Index

IBM Technical Disclosure Bulletins

US Pre-Grant Publication Full-Text Database

Search:

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## Search History

DATE: Thursday, December 29, 2005 Printable Copy Create Case

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side

DB=PGPB, USPT, USOC; PLUR=YES; OP=OR

L4 11 and L3

<u>L3</u> 710/305,33,74,1,315,316;709/228,230,250;711/149,100,145,211;713/1,100;704/7;365/63,189.08,230.05.ccls. 17 *DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR* 

<u>L2</u> L1

DB=PGPB, USPT, USOC; PLUR=YES; OP=OR

L1 (("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory)

#### Search Results -

Terms	Documents
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Database:

US Patents Full-Text Database US OCR Full-Text Database

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Search:

L2

Refine Search

Recall Text 4

Clear

Interrupt

## Search History

DATE: Thursday, December 29, 2005 Printable Copy Create Case

side by side DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR <u>L2</u> Ll

**Hit Count Set Name** 

DB=PGPB, USPT, USOC; PLUR=YES; OP=OR

<u>L1</u> (("low pin count" or LPC) near5 memory) same ((firmware or BIOS) near5 memory) 28

<u>L2</u>

result set

L1

**END OF SEARCH HISTORY** 

Set Name Ouery

#### Search Results -

Terms	Documents	
"1-bit" near5 "memory flag"	3	

US OCR Full-Text Database

Database: EPO Abstracts Database JPO Abstracts Database

Derwent World Patents Index

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IBM Technical Disclosure Bulletins

US Pre-Grant Publication Full-Text Database

Search:

L2	
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	<b>V</b>

Refine Search

Recall Text Clear

Interrupt

## Search History

DATE: Thursday, December 29, 2005 Printable Copy Create Case

Set Name Query

**Hit Count Set Name** 

side by side

result set

DB=PGPB, USPT, USOC; PLUR=YES; OP=OR

L2 "1-bit" near5 "memory flag"

<u>L2</u>

L1 "1-bit memory flag"

) <u>L1</u>

#### Search Results -

Terms	Documents		
"1-bit" near5 "memory flag"	1		

US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database Database: EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins L3

Recall Text 4

Search:

Clear

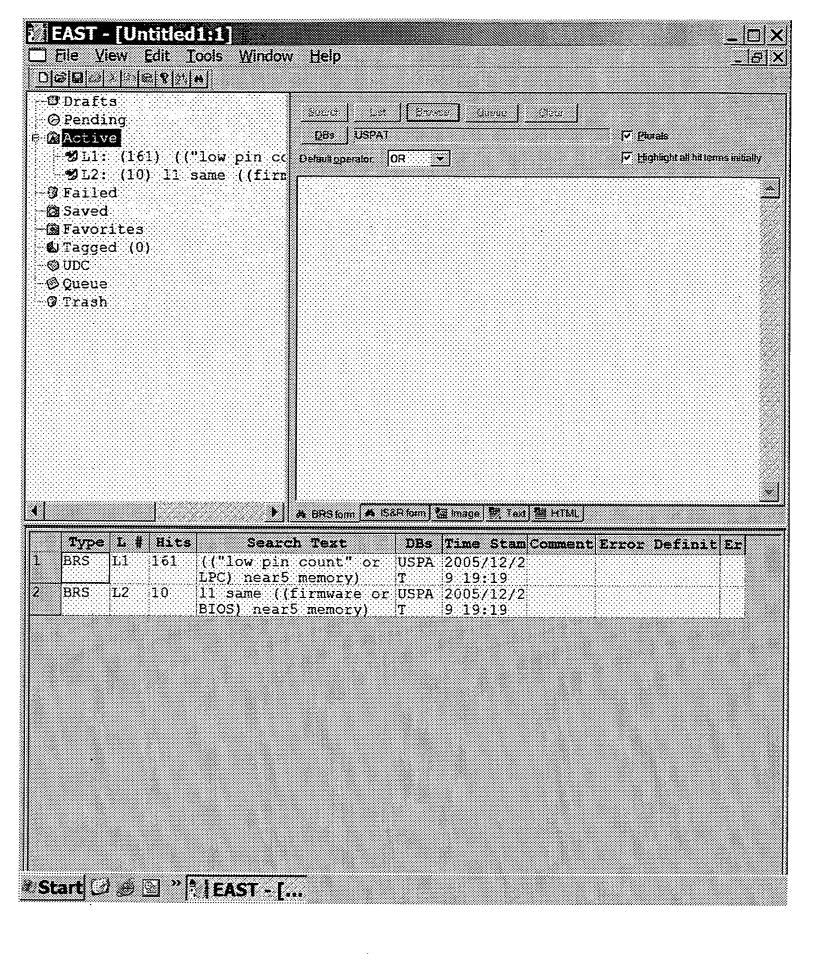
Interrupt

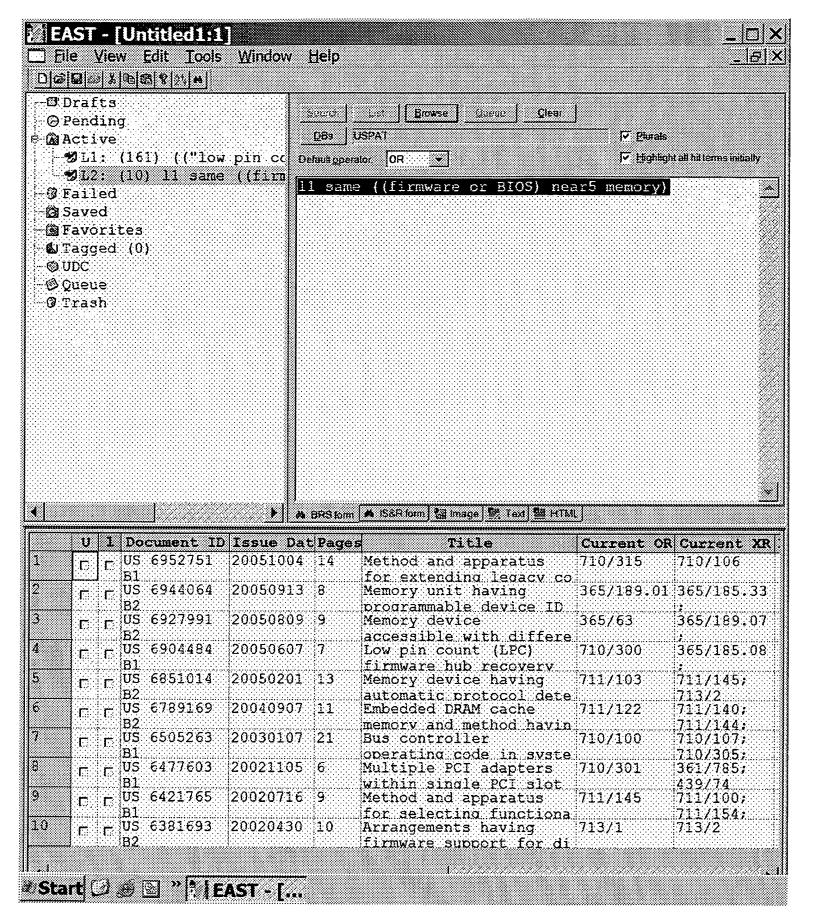
Refine Search

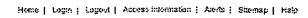
## Search History

DATE: Thursday, December 29, 2005 Printable Copy Create Case

Set Name Query **Hit Count** Set Name result set side by side DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR "1-bit" near5 "memory flag" L3 L3 DB=PGPB, USPT, USOC; PLUR=YES; OP=OR L2 "1-bit" near5 "memory flag" L2 "1-bit memory flag" Ll L1









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			5.	A 2000-MOPS embedded RIS Suzuki, K.; Daito, M.; Inoue, T Yamashina, M.; Solid-State Circuits, IEEE Jou Volume 34, Issue 7, July 199 Digital Object Identifier 10.110	.; Nadehara, K.; Nomura rnal of 19 Page(s):1010 - 1021	nmbus DRAM controller , M.; Mizuno, M.; Iima, T.; Sato,	S.; Fukuda, ʻ	T.; Arai, T.; Kuroda, I.;
				AbstractPlus   References   Fu	ıll Text: <u>PDF(</u> 512 KB)	eee Jnl		
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			7.	IEEE standard for scalable c	oherent interface (SCI)			

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